



In re application of: Levine et al.

Serial No.: 10/674,642

Filed: September 30, 2003

For: Method and Apparatus to **Autonomically Count Instruction Execution for Applications**

> 35525 PATENT TRADEMARK OFFICE CUSTOMER NUMBER

Group Art Unit: 2192 § Examiner: Chou, Andrew Y. Attorney Docket No.: AUS920030484US1 Certificate of Mailing Under 37 C.F.R. § 1.8(a) I hereby certify this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on June. By:

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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- Supplemental Information Disclosure Statement;
- Form PTO-1449;
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No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted

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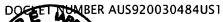
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Levine et al.

Serial No: 10/674,642

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Title: Method and Apparatus to

Autonomically Count Instruction Execution

for Applications

Group Art Unit: 2192

Intellectual Property Law Department International Business **Machines Corporation** 11400 Burnet Road Austin, Texas 78758

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. 1.97(g) the filing of this Supplemental Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56 (b) exists.

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Respectfully submitted,

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Substitute for form 1449 INFORMATION DISC

	Complete if Known
Application Number	10/674,642
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Chou, Andrew Y.
Art Unit	2192
Attorney Docket No.	AUS920030484US1

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Examiner Initials		Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where t Relevant Passages or Relevan Figures Appear	
	AA	6,149,318	11-21-2000	Chase et al.		
	AB	6,185,671 B1	02-06-2001	Pentovski et al.		
	AC	6,247,113 B1	06-12-2001	Jaggar		
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2011/11/15/15/16/2011	24: 5U88890TUH . I A			·		
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	AG	MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support Runtime Optimization", IEEE, 1999, pp. 136-147.				
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